



"John Croix"

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)

Scholar

Results 1 - 10 of 10 for "**John Croix**". (0.08 seconds)

Blade and razor: cell and interconnect delay analysis using current-based models - group of 9

»

JF Croix, DF Wong - DAC, 2003 - doi.ieeecomputersociety.org
 ... Using Current-Based Models John F. Croix Silicon Metrics Corporation 12710 Research
 Blvd. Suite 300 Austin, Texas 78759 **John.Croix@siliconmetrics.com** ...
 Cited by 9 - [Web Search](#)

A Fast And Accurate Technique To Optimize Characterization Tables For Logic Synthesis - group of 9 »

JF Croix, DF Wong - DESIGN AUTOMATION CONFERENCE, 1997 - doi.ieeecomputersociety.org
 Page 1. A Fast And Accurate Technique To Optimize Characterization Tables For
 Logic Synthesis John F. Croix Advanced Micro Devices, Inc. ...
 Cited by 6 - [Web Search](#) - [BL Direct](#)

[CITATION] The Need for Accurate Power Models for Deep Submicron IP reuse

JF Croix - Electronic Systems, 1999
 Cited by 3 - [Web Search](#)

West Indian Migration to the United States Virgin Islands: Demographic Impacts and Socioeconomic ... - group of 3 »

K de Albuquerque, JL McElroy - International Migration Review, 1982 - JSTOR
 ... Virgin Islands Immigration Thomas **John Croix** Total Before 1930 386 8 182 576
 1930-39 134 8 26 168 1940-49 250 13 40 303 1950-54 322 14 113 449 1955-59 735 52 ...
[Web Search](#)

The Shifting Sands Of DSM Characterization - group of 2 »

D MALINIAK - ELECTRONIC DESIGN, 2001 - mda.cuesta.com
 ... in the context of the design itself, not just a priori like a standard-cell library
 is characterized before you even build a chip," says **John Croix**, CTO at ...
 Cited by 1 - [View as HTML](#) - [Web Search](#)

Leakage Power Estimation and Minimization in VLSI Circuits - group of 2 »

WT Shiue - IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, 2001 - engr.oregonstate.edu
 ... employees Guruprasad Rao, Vess Johnson, Stephen King, Tamara Cryar, Hope Luedecke,
 Callan Carpenter, Shakir Abbas, **John Croix**, Scott Yore, and Paul Ballast. ...
 Cited by 11 - [View as HTML](#) - [Web Search](#) - [BL Direct](#)

LEAKAGE POWER ESTIMATION AND MINIMIZATION IN VLSI CIRCUITS

TX Austin - ieexplore.ieee.org
 ... Vess Johnson. Stephen King, Tamara Cryar. Hope Luedecke. Callan Carpenter. Shakir
 Abbas, **John Croix**. Scott Yore. and Paul Ballast. 7. REFERENCES Z. Chen. ...
[Web Search](#)

General Chair's Welcome - group of 5 »

G De Micheli - ieexplore.ieee.org
 Page 1. Page 2. General Chair's Welcome Welcome to thc 37th annual Design
 Automation Confrencce (DAC). DAC yearly attracts thousands ...
[Web Search](#)

RICE: Rapid Interconnect Circuit Evaluation Using AWE - group of 4 »

CL Ratzlaff, LT Pillage - IEEE Transactions on Computer-Aided Design of Integrated ..., 1994 -
ieeexplore.ieee.org

Page 1. IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND
SYSTEMS, VOL. 13, NO. 6, JUNE 1994 163 RICE: Rapid Interconnect ...

Cited by 75 - Web Search - BL Direct

[PS] A reusable framework for Web-based teleoperation of robotic devices - group of 2 »

S Ghiasi - 1999 - csel.cs.colorado.edu

Page 1. A Reusable Framework for Web-Based Teleoperation of Robotic Devices by S.

Ghiasi BA, University of Colorado at Boulder, 1996 A thesis submitted to the ...

View as HTML - Web Search

"John Croix"

Search

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2006 Google



library "John Croix"

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)
ScholarResults 1 - 5 of 5 for library "John Croix". (0.01 seconds)

Tip: Try removing quotes from your search to get more results.

A Fast And Accurate Technique To Optimize Characterization Tables For Logic Synthesis - group of 9 »

JF Croix, DF Wong - DESIGN AUTOMATION CONFERENCE, 1997 - doi.ieeecomputersociety.org

... accurately. Experimental results from the use of these techniques within AMD

for a Synopsys cell **library** is also presented. 1 Introduction ...Cited by 6 - Web Search - BL Direct
Blade and razor: cell and interconnect delay analysis using current-based models - group of 9

»

JF Croix, DF Wong - DAC, 2003 - doi.ieeecomputersociety.org

... Suite 300 Austin, Texas 78759 John.Croix@siliconmetrics.com ... models were created forcells in a 0.13µm, 1.5V produc- tion cell **library** using parasitically ...Cited by 9 - Web Search
The Shifting Sands Of DSM Characterization - group of 2 »

D MALINIAK - ELECTRONIC DESIGN, 2001 - mda.cuesta.com

... in the context of the design itself, not just a priori like a standard-cell **library**is characterized before you even build a chip," says **John Croix**, CTO at ...Cited by 1 - View as HTML - Web Search
General Chair's Welcome - group of 5 »

G De Micheli - ieeexplore.ieee.org

... or conference proceedings prior to 1978, or any SIGNewsletter at any time, and you

do NOT want this work to appear in the ACM Digital **Library**, please inform ...Web Search
[PS] A reusable framework for Web-based teleoperation of robotic devices - group of 2 »

S Ghiasi - 1999 - csel.cs.colorado.edu

Page 1. A Reusable Framework for Web-Based Teleoperation of Robotic Devices by S.

Ghiasi BA, University of Colorado at Boulder, 1996 A thesis submitted to the ...

View as HTML - Web Search

library "John Croix"

Search

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2006 Google

	Type	Ref #	Hits	Search Text
1	BRS	S187	51	(interface\$3 same communication\$2) and (programs same first)and (programs same second) and (programs same runtime) and (programs same third) and dataset and query\$3
2	BRS	S188	2333	(first adj programs) and (second adj programs) and (third adj programs)
3	BRS	S189	597	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2)
4	BRS	S190	3	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (program adj runtime)
5	BRS	S191	513	719/313.ccls.
6	BRS	S192	0	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (program adj runtime) and (electron\$6 same circuit)
7	BRS	S193	19	719/313.ccls. and (electron\$6 same circuit)
8	BRS	S195	0	719/313.ccls. and (electron\$6 adj circuit adj design)
9	BRS	S194	5	719/313.ccls. and (electron\$6 adj circuit)
10	BRS	S196	71	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (electron\$6 same circuit)
11	BRS	S197	3	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (electron\$6 same circuit same automation)

	DBs	Time Stamp	Comments	Error Definition
1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 12:56		
2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 12:57		
3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:07		
4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 14:40		
5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:06		
6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:05		
7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:07		
8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:07		
9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:07		
10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 13:08		
11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/02/21 15:51		

	Type	L #	Hits	Search Text
1	BRS	L2	0	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (program adj runtime) and (forth adj programs)
2	BRS	L3	0	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (program adj runtime) and (plug adj in)
3	BRS	L4	27	(dynamic adj link adj library) and (program adj runtime)
4	BRS	L5	0	(dynamic adj link adj library) and (program adj runtime) and (second adj application)
5	BRS	L6	1	(dynamic adj link adj library) and (program adj runtime) and (first adj application)
6	BRS	L7	0	(dynamic adj link adj library) and (program adj runtime) and (first adj application) and plug
7	BRS	L8	8337	(interface\$3 same communication\$2) and (programs same first)and (programs same second) and (integrated adj circuit)
8	BRS	L9	128	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (integrated adj circuit)
9	BRS	L10	9	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (integrated adj circuit) and (program same runtime)
10	BRS	L11	3	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (integrated adj circuit) and (program same runtime) and dataset
11	BRS	L12	9	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (integrated adj circuit) and (program same runtime)
12	BRS	L13	9	(first adj programs) and (second adj programs) and (third adj programs) and (interface same communication\$2) and (integrated adj circuit) and (program same runtime) and (data or datum)
13	BRS	L14	481	703/1.ccls.
14	BRS	L15	759	703/1.ccls.
15	BRS	L16	759	703/1.ccls.